



**REGULAR EXAMINATION TIMETABLE (REVISED), MAY, 2026**

**BACHELOR OF TECHNOLOGY (B. Tech.)**

**THIRD YEAR, SEMESTER – VI (For 2025-26 Batch Under Autonomy)**

**Timings : 10:00 a.m. To 12:00 p.m.**

**Date: 11<sup>th</sup> April, 2026**

Day & Date	Mechanical Engineering	Electronics & Telecommunication Engg	Information Technology	Computer Engineering	Computer Science & Engineering (Data Science)	Artificial Intelligence (AI) and Data Science	Artificial Intelligence and Machine Learning	Computer Science and Engineering (IOT and Cyber Security with Block Chain Technology)
Monday 18/05/2026	Design of Machine Elements	Radiating System	Software Engineering	Software Engineering	Natural Language Text Processing	MLOps	DevOps and MLOps	Blockchain for Cybersecurity
Wednesday 20/05/2026	Finite Element Analysis	Machine Learning & Applications	Machine Learning	Machine Learning	Machine Learning - III (Reinforcement Learning)	Deep Learning		Machine Learning
Friday 22/05/2026	Control Systems	Image Processing	Cryptography and Network Security	Security in IoT Ecosystem	Time Series Analysis	Large Language Models		IoT Data Analytics
					Analysis of AI Algorithms			Industrial Internet of Everything
Monday 25/05/2026	Automotive Systems	Data Analytics	Infrastructure Security	Big Data Analytics	Computer Vision	Advanced Data Structures	Agentic and Explainable AI	Software Engineering and Testing
	Artificial Intelligence and Machine Learning	Big Data Analytics						Image Processing and Computer Vision
	Refrigeration and Air Conditioning		Basics of IoT	Essentials of Generative AI and Prompt Engineering	Quantitative Portfolio Management	Computational Models for Biology		Game Design and Gamification
Wednesday 27/05/2026	Energy Source Management	Deep Learning	MLOps	Essentials of Generative AI and Prompt Engineering	Quantitative Portfolio Management	Computational Models for Biology	Game Design and Gamification	Time Series and Forecasting Analytics
	Advanced Robotics	Analog VLSI						
Friday 29/05/2026	Heat Transfer	Computer Networks	Big Data Analytics	Advanced Network Design	-	System Design	Advanced System Design	Network Security
				Distributed Databases				
				High Performance Computing				
Monday 01/06/2026	-	-	Cloud Computing	-	-	-	-	-

**Important Note: Changes if any, in the time table shall be communicated on the college website (www.djsce.ac.in).**

Ms. Alka Shukla  
 Controller of Examinations

Dr. Hari Vasudevan  
 Principal 13/4/26